

IN THE CLAIMS

- 1 1. (Canceled)
- 1 2. (Currently Amended) The arrangement of claim ~~1~~22, wherein said common bus is a CAN-  
2 bus.
- 1 3. (Currently Amended) The arrangement of claim ~~1~~22, wherein said input member  
2 comprises a control unit, a bus controller, bus driver, memory unit and an input signal  
3 controller.
- 1 4. (Original) The arrangement of claim 3, wherein the controller unit consists of a  
2 microprocessor or other data processing arrangement.
- 1 5. (Original) The arrangement of claim 3, wherein said bus controller is a CAN-bus controller  
2 and the bus driver is a CAN-bus driver.
- 1 6. (Currently Amended) The arrangement of claim ~~1~~22, wherein said output member comprises  
2 a control unit, a bus controller, bus driver, memory unit and an output signal controller.
- 1 7. (Original) The arrangement of claim 6, wherein the controller unit consists of a  
2 microprocessor or other data processing arrangement.
- 1 8. (Original) The arrangement of claim 6, wherein said bus controller is a CAN-bus controller  
2 and the bus driver is a CAN-bus driver.
- 1 9. (Currently Amended) The arrangement of claim ~~1~~22, wherein said at least one contact ~~input~~

2 ~~terminal~~ of the input member is so provided that they alter between an input and output ~~terminal~~  
3 contact state.

1 10. (Currently Amended) The arrangement of claim 9, wherein said input/output member  
2 comprises a number of input/output ~~terminals~~ contacts and at least one of said input/output  
3 ~~terminals~~ contacts is arranged as a common signal ~~terminal~~ contact.

1 11. (Currently Amended) The arrangement of claim 10, wherein said common signal has  
2 different states, determining different states for said input ~~terminal~~ contact.

1 12. (Currently Amended) The arrangement of claim 9, wherein each input ~~terminal~~ contact is  
2 connected to a normally open or normally closed switch, which state is determined by  
3 programming a control unit.

1 13. (Currently Amended) The arrangement of claim 9, wherein said states of said input  
2 ~~terminal~~ contact are determined to bistable or pulse.

1 14. (Currently Amended) The arrangement of claim ~~122~~, wherein said at least one contact of  
2 output member ~~terminal~~ is arranged such that it allows tuning a current limitation for said at least  
3 one output ~~terminal~~ contact.

1 15. (Currently Amended) The arrangement of claim ~~122~~, wherein said input/ and output  
2 members are connected to a common power supply line.

1 16. (Original) The arrangement of claim 15, wherein said common power supply line is  
2 arranged as a loop.

1 17. (Original) The arrangement of claim 15, wherein said common power supply line is  
2 connected to a power supply arrangement at each end.1

1 18. (Original) The arrangement of claim 17, wherein said common power supply line is  
2 provided with an arrangement for detecting an excess-current.

1 19. (Currently Amended) The arrangement of claim 122, wherein said common bus is used to  
2 communicate control commands and status messages between said input and output members.

1 20. (Currently Amended) The arrangement of claim 122, wherein input members are connected  
2 to switch and indicator groups, which are interconnected by means of a common signal line.

1 21. (Currently Amended) A method of controlling a plurality of controllable devices  
2 interconnected through ~~to~~ at least one common signaling bus, the method comprising:

3           arranging at least one input member and at least one output member, each input and  
4 output member having at least one input terminalcontact and at least one output terminalcontact  
5 and each contact -having an unique identity and each input and output member communicating  
6 through said common bus,

7           arranging said input member to receive a control signal from at least one control  
8 arrangement connected to said input terminalcontact of said input member,

9           upon reception of said control signal generating an action signal comprising an  
10 address corresponding to an unique identity of said output terminalcontact connected to at least  
11 one of said controllable devices, and

providing said action signal on said common bus by said input member to be received  
by said output member connected to at least one of said controllable devices.

22. (New) A network arrangement comprising:  
at least one common bus,  
an input member connected to said common bus,  
an output member connected to said common bus,  
said input member comprises at least one input contact defined with a unique identity,  
said output member comprises at least one output contact defined with a unique identity,  
said input member being adapted to receive an input signal through said at least one input  
contact,  
said output member being adapted to provide an output signal through said at least one output  
contact,

wherein said input member is arranged to generate an action signal transmitted  
over said buss from said input member to said output member, said action signal  
comprising an address corresponding to said unique identity of an output contact.